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APPLICANTS

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** CONTINUING DATA ***** *Verified and*
 This application is a CON of 09/577,671 05/25/2000 PAT 6,737,341

** FOREIGN APPLICATIONS ***** *Verified and*
 JAPAN 11-158089 06/04/1999

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
 ** 04/27/2004

Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY JAPAN	SHEETS DRAWING 27	TOTAL CLAIMS 13	INDEPENDENT CLAIMS 3
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Verified and Acknowledged
 Examiner's Signature *[Signature]* Initials

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TITLE
 Semiconductor integrated circuit device and method for manufacturing the same

FILING FEE	FEES: Authority has been given in Paper	<input checked="" type="checkbox"/> All Fees
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